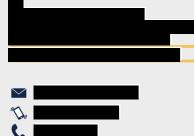


NICOLA ZANATTA CURRICULUM VITAE







SOFT SKILL

Autonomy 9/10 Self confidence 9/10 Flexibility/Adaptability 8/10 Resistance to stress 8/10 Ability to plan and organize 8/10 Managing information 8/10 Precision/Attention to details 9/10 Learn continuously 9/10 Achievement of objectives 9/10 Entrepreneurial spirit and initiative 8/10 Communication 8/10 Problem Solving 8/10 Leadership 8/10

FOREIGN L	ANGUA	\GE SI	KILLS	X	uro pass
MOTHER TONGUE(S): Italian					
	•		Ø	1º	Ø
ENGLISH GOOD	C1	C2	B2	B2	B2
DIGITAL COMPETENCES					

Self-assessment grid Correction Proficient user Communication Proficient user Content creation Independent user Safety Proficient user Problem solving Proficient user

EXPECTATIONS AND FEATURES OF THE DESIRED JOB

INTENTION TO CONTINUE STUDIES: Yes / Graduate studies

ECONOMIC SECTOR: 1. aeronautics, aerospace, shipbuilding / 2. computer science, data processing and acquisition / 3. communication and telecommunications PH.D. 2020 - 2024 ONGOING STUDIES



Career Goal

MASTER'S DEGREE 2017 - 2020

CERTIFIED TITLE

I want to apply the knowledge learned in my studies in electronic engineering working in the design and development of circuit for power electronics.

ACADEMIC STUDIES

Università degli Studi di PADOVA Faculty: Ph.D. School in Mechatronics And Product Innovation Engineering - University of Padova High-efficiency electronic power converters for extreme fast charging (xfc) stations Expected graduation date: 2024

Università degli Studi di PADOVA Dipartimento di Ingegneria dell'Informazione Ingegneria elettronica

LM-29 - 2nd level degree in Electronic engineering Dissertation/thesis title: Analysis and Efficiency Study of the DAB Converter Topology | Thesis supervisor: CALDOGNETTO TOMMASO | Dissertation/thesis keywords: DAB converter ZVS TPS Switching losses Efficiency

Age at graduation: 24 | Official duration: 2 years Final degree mark: **109/110** Graduation date: 20/04/2020

Università degli Studi di PADOVA Dipartimento di Ingegneria dell'Informazione Ingegneria elettronica

L-8 - 1st level degree in Information technology Dissertation/thesis title: ENERGY HARVESTING FOR WIRELESS ELECTRONICS | Thesis supervisor: TENTI PAOLO Age at graduation: 22 | Official duration: 3 years Final degree mark: **106/110** Graduation date: 19/09/2017

Industrial Vocational School ITIS Luigi Einaudi , MONTEBELLUNA (TV) School-leaving examination mark: **84/100** Kind of secondary school diploma: Italian secondary school diploma Kind of secondary school attended: Public school

WORK EXPERIENCES

Research collaborator FONDAZIONE STUDI UNIVERSITARI DI VICENZA

Computer science, data processing and acquisition PADOVA (PD) 05/2020 - 09/2020 **Main activities and responsibilities:** Research project on an innovative topology of high-efficiency dc/dc converter for extreme fast-chargers. The converter will be part of a multi-cell 350kW dc/dc converter. Sizing, parametric optimization, techniques for conduction and switching losses reduction, are active parts of the research activity.

Working as: employee collaborator





TECHNICAL CERTIFICATE

MONTEBELLUNA 2014

This CV contains confidential information collected by the Inter-universities Consortium AlmaLaurea. Full or partial reproduction and diffusion to third parties are strictly forbidden I hereby authorize the processing of the personal data contained in this CV in compliance with the European Regulation (UE) 2016/679. CAREER FIELD: 1. Engineering and design DESIRED JOB:

Designer&Developer of circuits for power electronics



Scholastic internship PIEMME IMPIANTI DI PIOVESAN MARCO & C.

Computer science, data processing and acquisition MONTEBELLUNA (TV) 06/2013 - 07/2013

Scholastic internship ELETTROTC

Computer science, data processing and acquisition TREVIGNANO (TV) 06/2012 - 07/2012

Hello! Ola!

Main activities and responsibilities: Servicing of air cooling systems, Assembly of PV panels, Installation of security systems. Employed as: intern/trainee - internship | Company sector: Manufacturing

Main activities and responsibilities: Assembling and wiring of electrical panels for industrial machines Employed as: intern/trainee - internship | Company sector: Manufacturing

FOREIGN LANGUAGE SKILLS

DIPLOMAS AND CERTIFICATES

English OLS english test, OLS Online Linguistic Support / Esasmus+, 18 Dec 2018 , **Europass level C1**



OFFICE AUTOMATION

APPLICATION SOFTWARE

COMPUTER PROGRAMMING

SYSTEMS AND NETWORKS

GRAPHICS AND MULTIMEDIA

INFORMATION TECHNOLOGY SKILLS

Office Suite: (Highly Specialised) | Presentation Software: Microsoft PowerPoint (Advanced) | Spreadsheets: Microsoft Excel (Advanced) | Web Browser: (Advanced) | Word Processors: Microsoft Word (Advanced) , TextMaker (Advanced)

CAD - Assisted Design: Altium Designer (Intermediate), Autodesk Eagle (Foundation), CST Microwave Studio (Intermediate)

Development Models: Plecs (Advanced), SPICE (Advanced) | Markup languages: HTML (Foundation), LaTeX (Advanced) | Programming languages: Assembly (Intermediate), C (Advanced), Java (Advanced), LabVIEW (Advanced), LabVIEW FPGA and Real-Time (Advanced), MATLAB (Advanced), Python (Advanced), Simulink (Advanced), VHDL (Foundation) | Web Programming: PHP (Foundation)

Network architecture: (Foundation) | **Operating systems:** Linux (Foundation)

DBMS: MySQL (Foundation)

Vector graphics editor: Adobe Illustrator (Advanced) , Ipe (Advanced) | Video Editing and Processing: Adobe Premiere Pro (Intermediate)

ICT CERTIFICATES

MANAGEMENT

DATA MANAGEMENT



NORWAY

2018

-

European Union program (Erasmus) At: NTNU

ECDL Full Standard AICA, 15/02/2013

Place: **Trondheim (Norway)** | Language: English | Duration: 4 (months)

STUDIES AND EXPERIENCES ABROAD

During my stay in NTNU I attended some courses gave me knowledge about: design and prototyping of an energy harvester device for biomedical usage; numerical calculation of Maxwell equations in simulators; satellite technology.



PROFESSIONAL ACCOLADES AND AWARDS

This CV contains confidential information collected by the Inter-universities Consortium AlmaLaurea. Full or partial reproduction and diffusion to third parties are strictly forbidden I hereby authorize the processing of the personal data contained in this CV in compliance with the European Regulation (UE) 2016/679. PRIZE

2017

PRIZE 2016





JOURNAL ARTICLES 2021

JOURNAL ARTICLES 2021

JOURNAL ARTICLES 2020

LESSONS/LECTURES

LESSONS/LECTURES

2021

2021

Mille e una lode 2017

The student was awarded a scholarship for outstanding academic performance within the academic year 2017/2018 edition of the 'Mille e una lode' project as one of the best 1000 students enrolled at Università degli Studi di Padova.

Mille e una lode 2016

The student was awarded a scholarship for outstanding academic performance within the academic year 2016/2017 edition of the 'Mille e una lode' project as one of the best 1000 students enrolled at Università degli Studi di Padova.

PUBLICATIONS

Nicola Zanatta; Tommaso Caldognetto; Giorgio Spiazzi; Paolo Mattavelli, A Two-Stage Isolated Resonant DC-DC Converter for Wide Voltage Range Operation Review: 2021 IEEE International Conference on Environment and Electrical Engineering an Publisher: 10.1109/EEEIC/ICPSEurope51590.2021.9584589 ieeexplore.ieee.org/document/9584589

S. Pistollato, N. Zanatta, T. Caldognetto, P. Mattavelli, A Low Complexity Algorithm for Efficiency Optimization of Dual Active Bridge Converters Review: Open Journal of Power Electronics Publisher: OJ-PEL-2020-0100 doi: 10.1109/OJPEL.2021.3053058 ieeexplore.ieee.org/document/9329137

Nicola Zanatta, Simone Pistollato, Tommaso Caldognetto, Paolo Mattavelli, Triple-Phase Shift Modulation and Tuning Technique to Improve the Efficiency of the Dual Active Bridge Converter Review: 2020 AEIT International Annual Conference (AEIT), Catania, Italy, 2020, pp. 1-6 Publisher: doi: 10.23919/AEIT50178.2020.9241157 ieeexplore.ieee.org/document/9241157

TEACHING ACTIVITIES

University of Padova, Vicenza Laboratory Assistant Laboratory assistant on analog filters. Main Professor: Prof. Paolo Mattavelli Character: Assistant

University of Padova , Vicenza Laboratory Assistant LTspice lesson for students of the Industrial Electronics course concerning a buck circuit with voltage/current controls. Laboratory assistant on buck board experiences. Main Professor: Prof. Paolo Mattavelli Character: Assistant



ATTACHMENTS

Ingegneria Elettronica Master's Degree in Ingegneria Elettronica at the University of Padova. bestr.it/award/show/PhKJ61uBRnSI_ECoCQqAEw?l...

International Mobility

International Mobility Badge bestr.it/award/show/Ela9_wcCQKK93Fm0z-zx1w?ln...

